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CT800 front ^[5]

CT800,

a [FLOSS dedicated chess computer](#) designed by [Rasmus Althoff](#). The CT800 features an [ARM Cortex M4 microcontroller](#) of an *Olimex H405* board ^[1] that operates up to 168 Mhz, with 1 MiB of [flash memory](#), 192+4 kiB of [SRAM](#), and multiple [I/O](#) and [USB](#) connectors, with a 4x20 character [text display](#), two [leds](#), and a 4x4 + 1 [keypad](#) as [peripherals](#) aka [user interface](#). [Voltage source](#) is due to four [NiMH rechargeable AA batteries](#) or external 5 V [DC power supply](#).

CT800's [firmware](#) is developed by Rasmus Althoff, released under the [GPL Version 3](#), the chess playing software is an ARM port of [George Georgopoulos' open source engine NG-play](#) ^[2] including a [KPK hitbase](#) by [Marcel van Kervinck](#) ^[3] and runs on both [Linux](#) or [Android](#).

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- [CT800 V1.01](#) by [Rasmus Althoff](#), [CCC](#), November 13, 2016
- [CT800 V1.10 released](#) by [Rasmus Althoff](#), [CCC](#), June 06, 2017
- [CT800 V1.11 release + Winboard-version](#) by [Rasmus Althoff](#), [CCC](#), June 17, 2017
- [CT800 V1.12 released](#) by [Rasmus Althoff](#), [CCC](#), October 14, 2017
- [CT800 engine: Android problems?](#) by [Rasmus Althoff](#), [CCC](#), January 08, 2018 » [Android](#)

External Links

- [CT800 Chess Computer](#)
- [Manual CT800](#) (pdf)
- [CT800 – embedded FLOSS Chess computer made with STM32-H405 OSHW board | olimex](#)

References

1. [^ Olimex STM32-H405](#)
2. [^ CT800 Summary](#)
3. [^ Yet another KPK endgame table generator: pfkpk](#) by [Marcel van Kervinck](#), [CCC](#), September 05, 2015
4. [^ kervinck/pfkpk · GitHub](#)
5. [^ CT800 Gallery](#)

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