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Chess Champion Mark IV <sup>[3]</sup>

**Chess Champion Mark IV**, (Chess Champion Super System Mark IV) a [dedicated chess computer](#) manufactured and released by [SciSys](#) in 1981 as successor of the [Chess Champion Super System III](#). The computer had a [6502](#) CPU @ 2MHz with a 12 KiB [Philidor Software](#) program written by [Mike Johnson](#), and was superseded by the [Chess Champion Mark V](#) later in 81 <sup>[1]</sup>. Chess Champion Mark IV participated at the [CPWTIPC 1981](#), where it lost the last round by default because the computer was stolen after round 6 <sup>[2]</sup>.

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## Publications

- [Chess Champion Super System MK IV](#) (pdf) by [Hein Veldhuis](#)

## External Links

- [Chess Champion Super System IV](#) from [Chess Computer UK](#) by [Mike Watters](#)
- [SciSys Chess Champion Super System MK IV](#) from [Schachcomputer.info Wiki](#)

## References

1. <sup>^</sup> [Chess Champion Super System IV](#) from [Chess Computer UK](#) by [Mike Watters](#)
2. <sup>^</sup> [Kevin O'Connell](#) (1981). *MicroChess - Paris Tournament*. [Personal Computer World](#), August 1981
3. <sup>^</sup> [5.Scisys/Saitek | Flickr - Fotosharing](#) by [Chewbanta](#)

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