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### **Mephisto Polgar,**

a [dedicated chess computer module](#) for [Mephisto module systems](#) by [Hegener & Glaser](#) launched in 1989. The [Rebel](#) based 8-bit program by [Ed Schröder](#) run on a [65C02 CMOS](#) processor. The same program appeared in the *Mephisto Academy*, a computer with focus on didactics and teaching chess, also dedicated to the Polgar sisters [Susan](#), [Sofia](#) and [Judit](#).

## See also

- [ChessMachine](#)
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- [Mephisto MM IV](#)
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- [Mephisto RISC](#)
- [Rebel](#)

## Publications

- [Helmut Pfleger](#), [Gerd Treppner](#), [Helmut Weigel](#) (1989). [Academy, Buch für Mephisto ....](#). [Schachversand Niggemann](#) (German)

## External Links

- [Module Polgar](#) from [The Spacious Mind](#)
- [Mephisto Polgar](#) from [Schachcomputer.info Wiki](#) (German)
- [Mephisto Polgar 10 MHz](#) from [Schachcomputer.info Wiki](#) (German)
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