

[Home](#) * [Engines](#) * **President Chess**



President Chess ^[3]

President Chess, (President Turbo)

a prototype of a [dedicated chess computer](#) by [SciSys](#) with a [Sensory Board](#) and an early program by [Julio Kaplan](#), somewhere between [Sensor Chess](#) and [Superstar](#). The computer has a [6502A](#) processor running at 2 MHz with 18 KiB [ROM](#) and 2 KiB [RAM](#). It played the [3rd European MCC 1982](#) in [London](#) under the name President Turbo ^[1]. It was never released, effectively superceded by the [Superstar 28K](#) in October 1983. A few test specimen are valuable collector's items ^[2].

Table of Contents

[See also](#)

[Forum Posts](#)

[External Links](#)

[References](#)

[What links here?](#)

See also

- [Saitek President](#)
- [Sensor Chess](#)
- [Superstar](#)
- [Turbostar](#)

Forum Posts

- [Collector's Corner...Scisys President Chess](#) by [Steve B. Hiarcs Forum](#), July 10, 2014

External Links

- [President Chess](#) from [Chess Computer UK](#) by [Mike Watters](#)
- [SciSys - President Chess \(prototype\)](#) (pdf) by [Hein Veldhuis](#)

References

1. [^ Tony Harrington](#) (1983). [The third European Microcomputer Chess tournament](#). [Creative Computing](#), Vol. 9, No. 1
2. [^ President Chess](#) from [Chess Computer UK](#) by [Mike Watters](#)
3. [^ Image](#) from [SciSys - President Chess \(prototype\)](#) (pdf) by [Hein Veldhuis](#)

What links here?

Page	Date Edited
Engines	Mar 10, 2018
European MCC 1982	May 22, 2016
Heuristic Software	May 4, 2013
President Chess	Jun 11, 2016
Saitek	Jun 11, 2016
Saitek President	Jun 11, 2016
Sensor Chess	Jun 7, 2013
Superstar	Jan 8, 2016

[Up one level](#)